	Application No.	Applicant(s)	
	10/609,493	SHINOTSUKA ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Aaron W. Carter	2624	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	pears on the cover sheet wi S (OR REMAINS) CLOSED in S) or other appropriate commit RIGHTS. This application is s	th the correspondence address n this application. If not included unication will be mailed in due course. THIS	ve
1. $\boxtimes$ This communication is responsive to <u>12/1/06</u> .			
2. ⊠ The allowed claim(s) is/are <u>1,2,6-8 and 12</u> .			
<ul> <li>3. Acknowledgment is made of a claim for foreign priority unally all blooms. Some* closed None of the:</li> <li>1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents.</li> </ul>	re been received. re been received in Application	on No	
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONI THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirements	
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give</li> </ol>	mitted. Note the attached EX/ ves reason(s) why the oath o	AMINER'S AMENDMENT or NOTICE OF declaration is deficient.	
5. CORRECTED DRAWINGS ( as "replacement sheets") mu	ıst be submitted.		
(a) $\square$ including changes required by the Notice of Draftsper	rson's Patent Drawing Review	v ( PTO-948) attached	
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date	<b></b>		
(b) ☐ including changes required by the attached Examiner Paper No./Mail Date	's Amendment / Comment or	in the Office action of	
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in			
<ol> <li>DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT</li> </ol>	OSIT OF BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	ERIAL must be submitted. Note the DLOGICAL MATERIAL.	
Attachment(s)			
1. Notice of References Cited (PTO-892)	5. Notice of In	formal Patent Application	
2.  Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413),	
Information Disclosure Statements (PTO/SB/08),     Paper No./Mail Date		/Mail Date Amendment/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's 9. □ Other	Statement of Reasons for Allowance	
	SUPERWS BY	SOE WU PATENT EXAMINER	

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Miles Yamanaka (45,665) on January 2, 2007.

The application has been amended as follows:

In the specification on page 4, please make the following changes:

In line 16, please replace the number – "22" – with the number – "23"-.

In line 18, please replace the number – "23" – with the number – "24"-.

In line 20, please replace the number – "24" – with the number – "25"-.

**DETAILED ACTION** 

1. This action is responsive to papers filed on December 1, 2006.

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### Response to Amendment

2. In response to applicant's amendment received on December 1, 2006, all requested changes to the claims have been entered. Claims 3-5 and 9-11 have been cancelled.

#### Terminal Disclaimer

3. The terminal disclaimer filed on December 1, 2006 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of 6/24/03 has been reviewed and is accepted. The terminal disclaimer has been recorded.

## **Priority**

4. Acknowledgment is made of applicant's claim for foreign priority based on an application filed in Japan on 12/26/2000. It is noted, however, that applicant has not filed a certified copy of the 2000-404832 application as required by 35 U.S.C. 119(b).

# Allowable Subject Matter

5. Claims 1, 2, 6-8 and 12 are allowed.

The following is an examiner's statement of reasons for allowance:

6. As to claims 1 and 7, none of the prior art teach or fairly suggest the limitation of "a control means for discharging a remaining electric charge of a parasitic capacitor of the photo diode by preliminarily charging a drain voltage of the MOS transistor", in combination with the

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other limitations of the claims. The prior art of Paik discloses image processing for emphasizing a specified luminance area of an image taken by an image sensor (column 4, lines 41-48, wherein a window corresponds to a specified luminance area and brightness is synonymous for luminance), wherein any plural luminance areas of the image are selected (column 4, lines 1-25 and 41-48, wherein the multiple windows corresponds to any plural luminance areas) and sensor outputs corresponding to the selected luminance areas are emphasized by using an image sensor output characteristic conversion table to increase a contrast of each image portion in each of the selected luminance areas (column 4, lines 41-60 and column 5, lines 20-22, wherein brightness levels correspond sensor outputs corresponding to the selected luminance areas, the look-up table corresponds to the conversion table and improving contrast corresponds to increasing contrast). The prior art of Yonemoto discloses a light sensor circuit representing at least one unit pixel of an image sensor has a logarithmic output characteristic (column 12, lines 14-22, wherein since the transistor is operating in the weak inverse region, it is providing a logarithmic output characteristic) and a light sensor circuit representing one unit pixel of the image sensor comprises a MOS transistor for converting a current flowing in a photo diode into a voltage signal having a logarithmic characteristic in a weak inverse state (column 12, lines 14-22, wherein since the transistor is operating in the weak inverse region, it is providing a logarithmic output characteristic). However, neither Paik nor Yonemoto disclose a control means for discharging a remaining electric charge of a parasitic capacitor of the photo diode by preliminarily charging a drain voltage of the MOS transistor, as disclosed in the limitations of claims 1 and 7.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Aaron W. Carter whose telephone number is (571) 272-7445. The examiner can normally be reached on 8am - 4:30 am (Mon. - Fri.).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jingge Wu can be reached on (571) 272-7429. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

AWS

SUPERVISORY PATENT EXAMINER